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NOTICE OF ALLOWANCE AND FEE(S) DUE

49684

7590

03/03/2008

IMPJ - THELEN REID BROWN RAYSMAN & STEINER LLP P.O. BOX 640640 SAN JOSE, CA 95164-0640 EXAMINER
HILTUNEN, THOMAS J
ART UNIT PAPER NUMBER

2816 DATE MAILED: 03/03/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/814,866	03/30/2004	Christopher J. Diorio	IMPJ-0027B	5628

TITLE OF INVENTION: REWRITEABLE ELECTRONIC FUSES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1440	\$300	\$0	\$1740	06/03/2008

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

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appropriate. All further indicated unless correcte maintenance fee notificat	correspondence including descriptions or directed otherwise constants.	g the Patent, advance or erwise in Block 1, by (a	ders and notification a) specifying a new co	of m orresp	aintenance fees wi condence address;	ll be and/or	mailed to the current (b) indicating a sepa	correspondence address as rate "FEE ADDRESS" for
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IMPJ - THELF P.O. BOX 64064 SAN JOSE, CA	10	N RAYSMAN & S	STEINER LLP	I here State addre trans	eby certify that this s Postal Service wi essed to the Mail mitted to the USPT	Fee(s th suf Stop O (57	b) Transmittal is being ficient postage for firs ISSUE FEE address 1) 273-2885, on the day	deposited with the United t class mail in an envelope above, or being facsimile tte indicated below.
								(Depositor's name)
								(Signature)
								(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVEN	ГOR		ATTO:	RNEY DOCKET NO.	CONFIRMATION NO.
10/814,866	03/30/2004	•	Christopher J. Diori	io	•		IMPJ-0027B	5628
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nonprovisional	NO	\$1440	\$300		\$0		\$1740	06/03/2008
EXAM	INER	ART UNIT	CLASS-SUBCLASS					
HILTUNEN,	THOMAS J	2816	327-525000					
"Fee Address" indi PTO/SB/47; Rev 03-0 Number is required. 3. ASSIGNEE NAME ADDRESSE NOTE: Unl	ess an assignee is identi	Indication form ed. Use of a Customer TO BE PRINTED ON The field below, no assignee	data will appear on th	native ingle or ag attorn I be p r type ne par	firm (having as a gent) and the name neys or agents. If norinted.	memb s of u o nam	er a 2 o to e is 3	ocument has been filed for
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4a. The following fee(s) are submitted: ☐ Issue Fee ☐ Publication Fee (No small entity discount permitted) ☐ Advance Order - # of Copies			4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above) ☐ A check is enclosed. ☐ Payment by credit card. Form PTO-2038 is attached. ☐ The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number (enclose an extra copy of this form).					
••	s SMALL ENTITY statu	s. See 37 CFR 1.27.	• •	_	_		CITY status. See 37 CF	
NOTE: The Issue Fee and interest as shown by the r	l Publication Fee (if requecords of the United Sta	nired) will not be accepte tes Patent and Trademark	d from anyone other the Office.	an th	e applicant; a regis	tered a	attorney or agent; or th	e assignee or other party in
Authorized Signature					Date			
Typed or printed name This collection of information is required by 37 CFR 1.311. The informa				Registration No.				
This collection of informan application. Confident submitting the completed his form and/or suggesting V450. Alexandria. V	ation is required by 37 C iality is governed by 35 application form to the ons for reducing this bur irginia 22313-1450. DO	FR 1.311. The informatic U.S.C. 122 and 37 CFR USPTO. Time will vary den, should be sent to the NOT SEND FEES OR	on is required to obtain 1.14. This collection is depending upon the i e Chief Information O COMPLETED FORM	or re s esti- ndivi fficer S TO	tain a benefit by th mated to take 12 m dual case. Any cor , U.S. Patent and T THIS ADDRESS.	e publ inutes nment Traden SENI	ic which is to file (and to complete, includin s on the amount of tir nark Office, U.S. Depa D TO: Commissioner I	by the USPTO to process) g gathering, preparing, and ne you require to complete rtment of Commerce, P.O. or Patents, P.O. Box 1450,

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10/814,866	10/814,866 03/30/2004 Christopher J. Diorio		IMPJ-0027B 5628			
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IMPJ - THELEN	I REID BROWN RA	HILTUNEN, THOMAS J				
P.O. BOX 640640		ART UNIT	PAPER NUMBER			
SAN JOSE, CA 95164-0640			2816			
			DATE MAILED: 03/03/2008			

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 139 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 139 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)			
	10/814,866	DIORIO ET AL.			
Notice of Allowability	Examiner	Art Unit			
	Thomas J. Hiltunen	2816			
	Thomas J. Hillunen	2010			
The MAILING DATE of this communication apperall claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	plication. If not included will be mailed in due course. THIS			
1. X This communication is responsive to amendment filed 11 F	<u>February 2008</u> .				
2. 🔀 The allowed claim(s) is/are <u>14,16,18-22,27,28,30,33,39-44</u>	,49,50,54,55,74-84,87-99 and 102-1	<u>113</u> .			
3. ☐ Acknowledgment is made of a claim for foreign priority un a) ☐ All _ b) ☐ Some* c) ☐ None of the:					
 Certified copies of the priority documents have 					
2. Certified copies of the priority documents have	· · · —				
3. Copies of the certified copies of the priority doc	cuments have been received in this	national stage application from the			
International Bureau (PCT Rule 17.2(a)).					
* Certified copies not received:					
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requirements			
 A SUBSTITUTE OATH OR DECLARATION must be submi INFORMAL PATENT APPLICATION (PTO-152) which give 					
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.				
(a) \square including changes required by the Notice of Draftspers	on's Patent Drawing Review (PTO-	948) attached			
1) ☐ hereto or 2) ☐ to Paper No./Mail Date					
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in the C	Office action of			
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the					
6. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT					
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5.	ratent Application			
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary				
Paper No./Mail Date 3. ☐ Information Disclosure Statements (PTO/SB/08), 7. ☐ Examiner's Amendment/Comment					
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit		8. X Examiner's Statement of Reasons for Allowance			
of Biological Material	9.				

Allowable Subject Matter

Claims 14, 16, 18-22, 27, 28, 30, 33, 39-44, 49, 50, 54, 55, 74-84, 87-99 and 102-113 are allowed.

The following is an examiner's statement of reasons for allowance:

With respect to claims 14, 16, 18-22, 27, 28 and 30, while it is well known to compose a cross coupled inverter latch from transistors that have different gate-length to width ratios, for instance see Fig. 3 of Wilson (USPN 6,06,919), there is no cited art that discloses the specific master fuse circuit comprising a nonvolatile memory element having a floating gate that determines the memory value and a cross-coupled inverter latch having a transistor of one inverter of the cross-coupled inverter latch with a gate-width-to-length ratio that is larger than another transistor of the other inverter, wherein the master latch controls the specific slave latch as recited in claim 14. For instance, Pascucci (USPN 5,851,762) fails to disclose the specific "slave latch circuit" controlled by a "master latch" as recited in claim 14. Furthermore Santin et al. (USPN 6,654,272) fails to disclose such sizing of the transistors of the latch circuit of Fig. 1 and there is no seen motivation to compose Santin et al.'s latch from differently sized transistors. Additionally, Wilson fails to disclose any nonvolatile memory element or any "fuse" circuit connected as recited in claim 14.

With respect to claims 33, 39-44, 49, 50, 54 and 55, while it is well known to compose a cross coupled inverter latch that includes multiple capacitors connected to ground and the supply voltage, for instance see Fig. 2 of Wilson (USPN 6,06,919), there is no cited art that discloses the specific master fuse circuit comprising a nonvolatile

memory element having a floating gate that determines the memory value and having a master latch with a first output being capacitively coupled to a first source of a fixed voltage and a second output being capacitively coupled to a second source of a fixed

voltage that is the same as the first, wherein the master latch controls the specific slave

Page 3

latch as recited in claim 33.

With respect to claims 74-83, while it is well known to compose a cross coupled inverter latch from transistors that have different doping levels, there is no cited art that discloses the specific master fuse circuit comprising a nonvolatile memory element having a floating gate that determines the memory value and a cross-coupled inverter latch with a transistor of one inverter having a channel doping level different than another transistor of the other inverter, wherein the master latch controls the specific slave latch as recited in claim 74.

With respect to claim 84 and 87-98, while it is well known to compose a cross coupled inverter latch from transistors that have different gate-length to width ratios, for instance see Fig. 3 of Wilson (USPN 6,06,919), there is no cited art that discloses the specific master fuse circuit comprising a nonvolatile memory element having a floating gate that determines the memory value and a cross-coupled inverter latch having a transistor of one inverter of the cross-coupled inverter latch with a gate-width-to-length ratio that is larger than another transistor of the other inverter, wherein the master latch controls the specific slave latch as recited in claim 84.

With respect to claim 99 and 102-113, while it is well known to compose a cross coupled inverter latch from transistors that have different doping levels, there is no cited art that discloses the specific master fuse circuit comprising a nonvolatile memory element having a floating gate that determines the memory value and a cross-coupled inverter latch with a transistor of one inverter having a channel doping level different than another transistor of the other inverter, wherein the master latch controls the specific slave latch as recited in claim 99.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thomas J. Hiltunen whose telephone number is (571)272-5525. The examiner can normally be reached on Mondays - Fridays from 8:00am to 4:30pm. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Drew Richards, can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

Application/Control Number: 10/814,866 Page 5

Art Unit: 2816

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TH February 22, 2008

/N. Drew Richards/

Supervisory Patent Examiner, Art Unit 2816